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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: YOSHIYUKI ANDO

Examiner: Miller, Craig S.

Serial No.: 10/707,450

Group Art Unit: 2857

Filed: December 15, 2003

For: INTEGRATED CIRCUITS HAVING
POST-SILICON ADJUSTMENT CONTROL

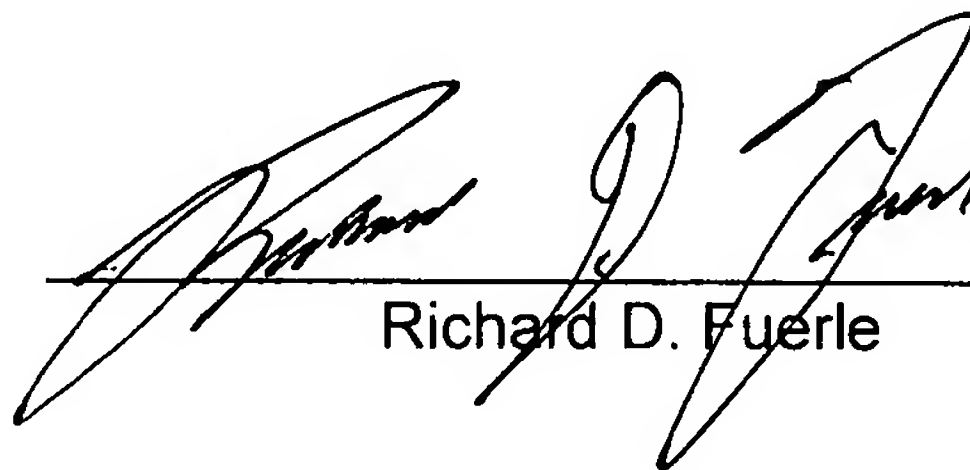
The Commissioner of Patents
and Trademarks
Alexandria, VA 22313

FEE LETTER

Sir:

A check in the amount of \$10 payable to the Commissioner of Patents is enclosed
as additional fee for the disclaimer filed February 15, 2005.

The undersigned is an attorney of record.


Richard D. Fuerle

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March 11, 2005

CASE YA06

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